



Signature

**IN UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No.: 7,198,980

Docket No: 2269-4886.1US

Issue Date: April 3, 2007

Patentee: Tongbi Jiang et al.

Title: METHODS FOR ASSEMBLING MULTIPLE SEMICONDUCTOR DEVICES

**REQUEST FOR CERTIFICATION OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

**As the error is that of the Patent Office, it is believed that no fee is due.**

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

**Customer Number: 26809**

Date: December 20, 2007

By:



Russ Sliter  
Reg. No. 39,838

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 20<sup>th</sup> day of December 2007

Kyrstin Ryan



\_\_\_\_\_  
Name

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Signature

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,198,980

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DATED : April 3, 2007

INVENTOR(S) : Jiang et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 3, below "5,977,640 A \* 11/1999 Bertin et al. 257/777" insert - - 5,998,859 7/1999 Griswold et al. - -.